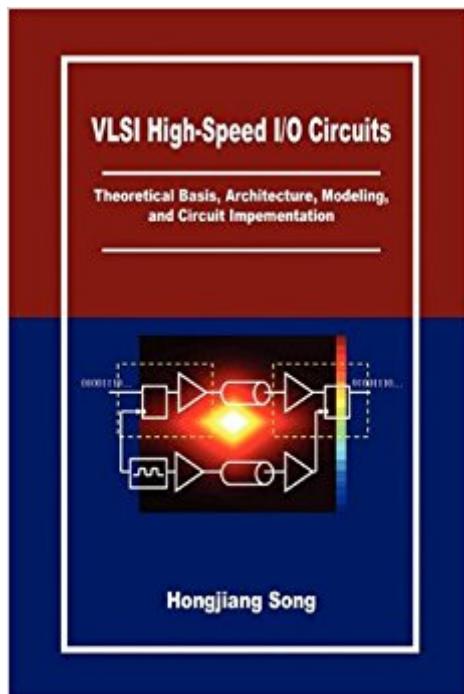


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# VLSI High-Speed I/O Circuits



## Synopsis

This book is based on the class notes of a VLSI design course the author offered in Electrical Engineering Department at Arizona State University. The materials are organized into twenty-one special topics covering various aspects of analysis, modeling, and implementation of VLSI high-speed I/O circuits, such as prototype timing models, jitter analysis, transmitter, receiver, equalizer, phase-locked loop (PLL), and data recovery circuit designs.

## Book Information

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No matter you are a VLSI high-speed IO circuit designer, a system engineer dealing with high-speed IO blocks, or just want to learn the basics and fundamentals of high-speed IO circuits, this is the best book you want to 100% put on your bookshelf. Before you decide to buy this book, noting that this book covers a very, very BROAD AREAS including Theoretical Basics (IO prototype model, IO delay effects and variations, and statistical VLSI IO delay variation model which is so important for modern BER

Best serial link high speed I/O design so far. Covers almost every aspect of the system you could think of, RX, TX, clock distribution, and so on, and gives you a full understanding of the system and the jitter budget, and a deep understanding of how to design all the related circuits too. A must own book if you are in this area.

Good as a handbook, definitely can not be used a textbook.

It covers various topics with regard to high speed I/O circuit design while providing good theoretical and practical perspectives. I personally appreciate you for referencing my technical paper at pages 182 and 293.

I have all the three books written by Hongjiang Song and his way of explaining things is very good. I think the writing a book is an ART... just because one is very educated in the field doesn't mean that he writes a good book. This book is very written for I/Os and is very good.... please do buy this book and it is not very costly ;-)

Not a great book for IO circuit design self-study.

This book covers a wide range of material with mathematical models that would have taken forever to find in journals and papers. A good book to keep for high speed I/O design

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